**Move net**

Перенос цепи в папку

**Cинтаксис**

sigrity::move net -nets {net1 net2 net3 ...}

***Parameters***

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| **Цель** | **Description** | **Type** | **Optional** | **Value Required** | **Default Value** |
| nets | Цепи которые нужно перенести | string | FALSE | - | - |

**Пример:**

sigrity::move net {D27\_CLK} {D27\_P} {!} – перенос цепи D27\_P в папку D27\_CLK

### Compare Netlists

**Syntax:**

sigrity::do NetlistCompare {circuit name 1} {circuit name 2} {comparison result}

Where, circuit name 1 and circuit name 2 are the paths to the two .ckt files to be compared and comparison result is the .csv file to be created.

**Example:**

sigrity::do NetlistCompare {C:/Users/Sim1/Pinbased\_PinBaseSPICE.ckt} {C:/Users/Sim2/Px2\_PinBaseSPICE.ckt} {C:/Users/output}

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### sigrity::querydetails

Use the sigrity::querydetails Tcl command for the following purposes:

sigrity::querydetails ckt -name {D21}

**export ports**

Use the following command to save automatically in a .csv file either the package and circuit

ports in PowerSI, or the VRM and impedance ports in OptimizePI.

***Syntax***

sigrity::export ports -share -filename {file path}

***Example***

sigrity::export ports -share -filename {D:\working\case\_1.csv} (Save

the ports to the file "D:\working\case\_1.csv".)